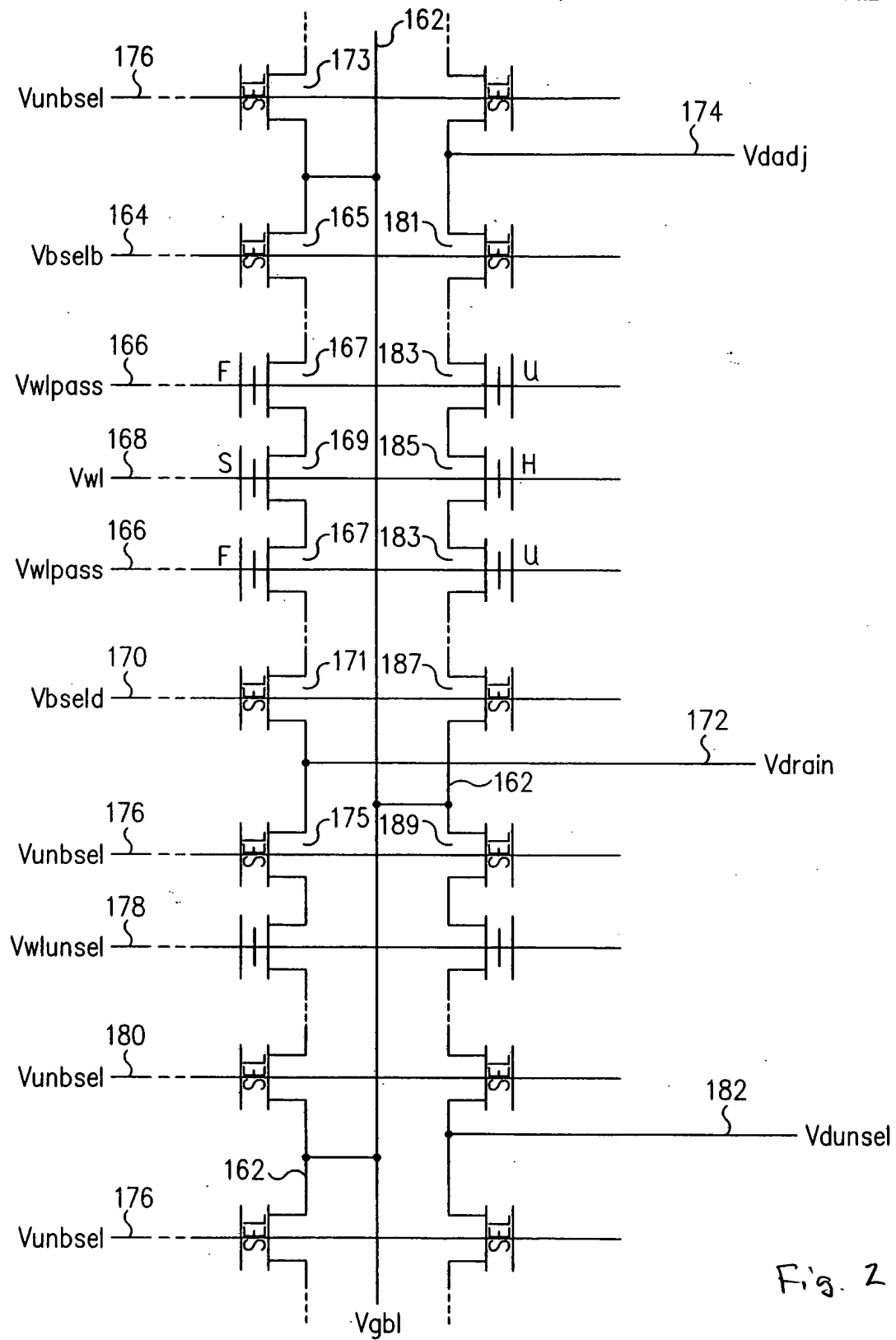


Fig. 1



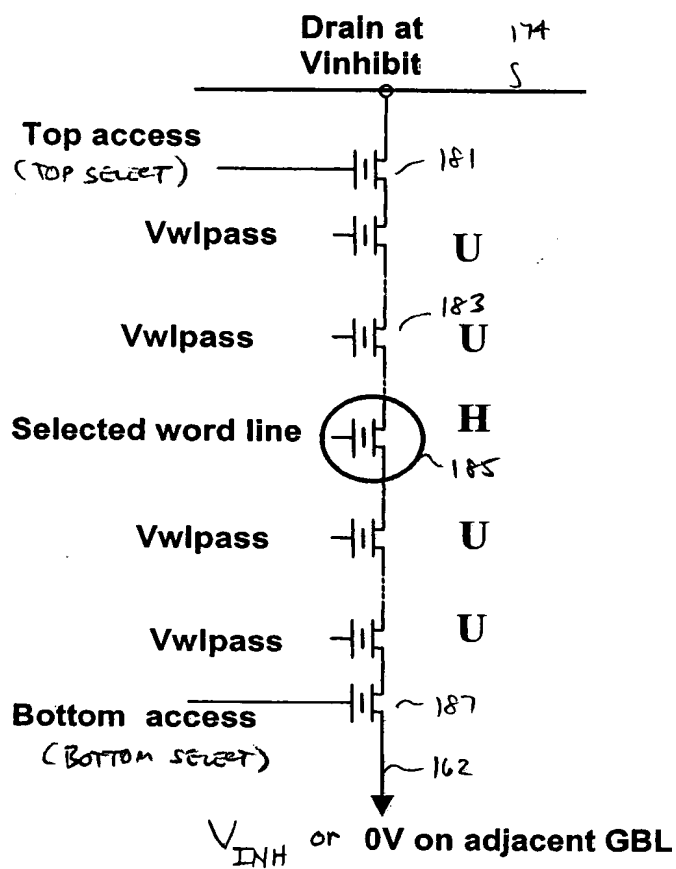


Fig. 3

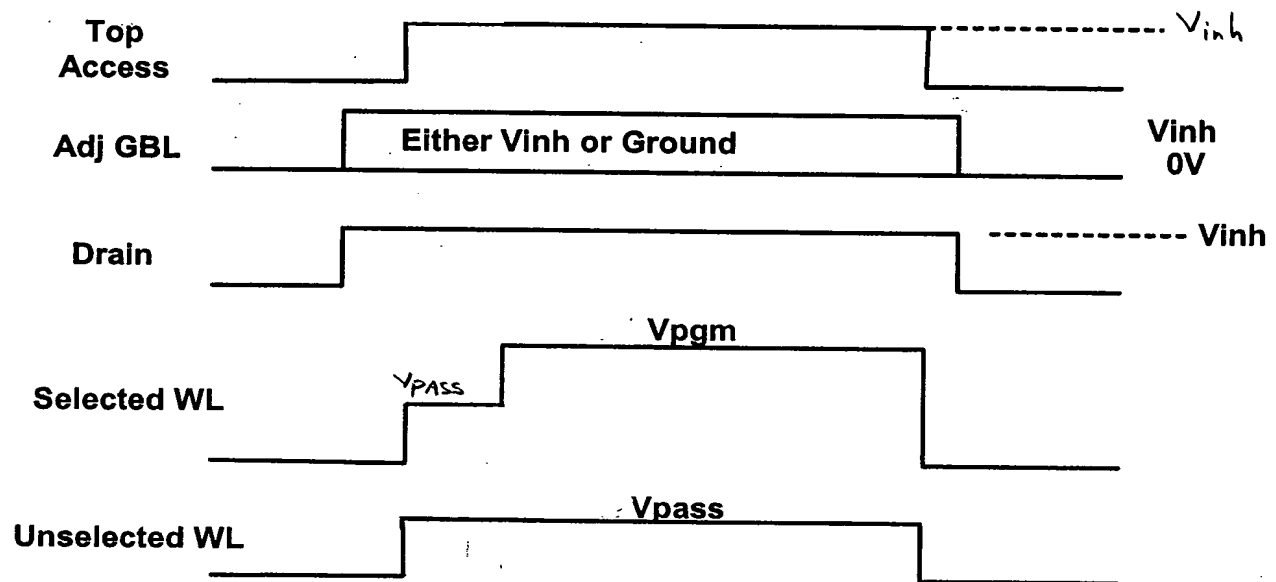


Fig. 4

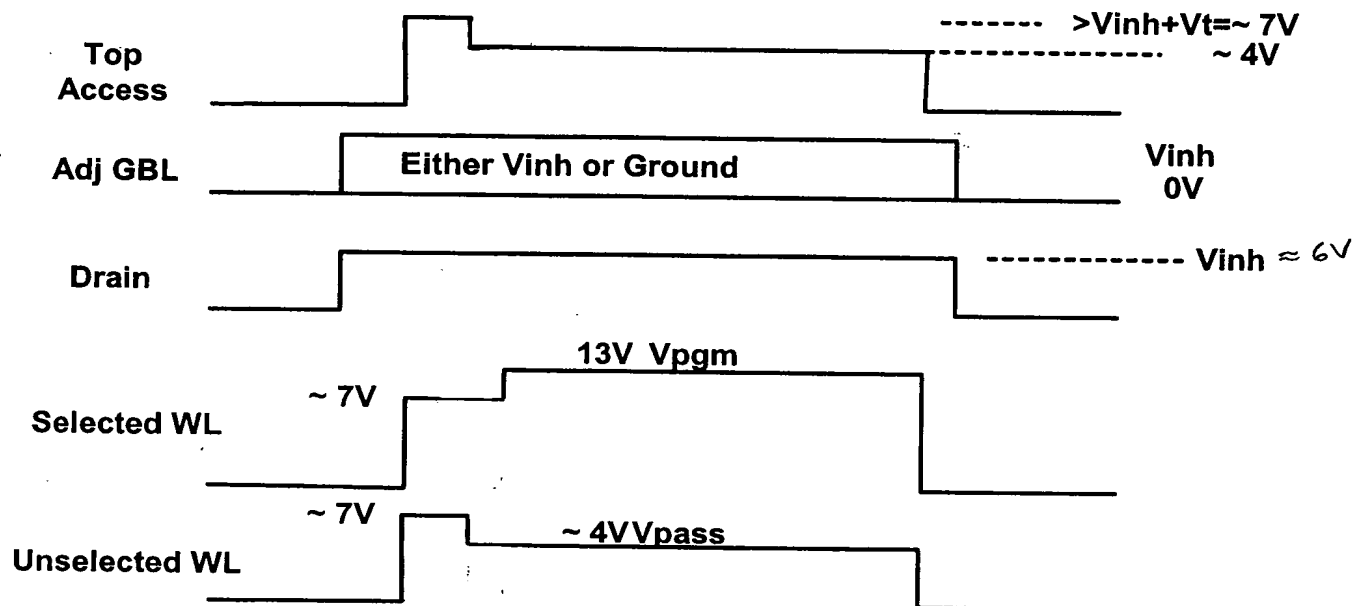


Fig. 5

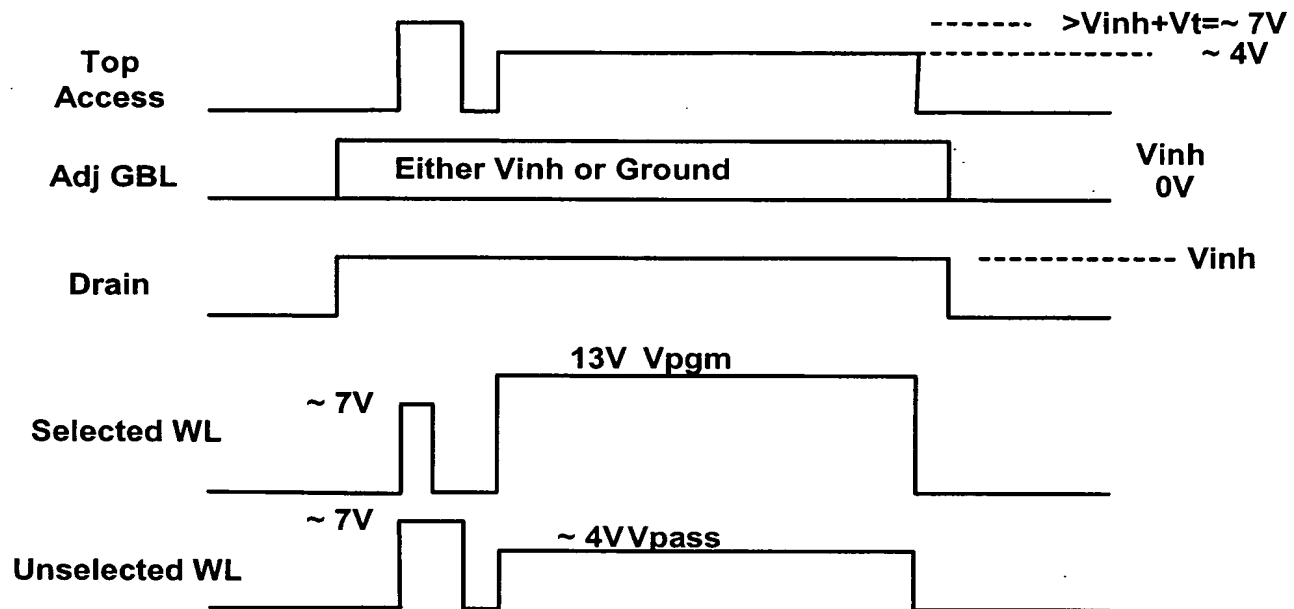


Fig. 6

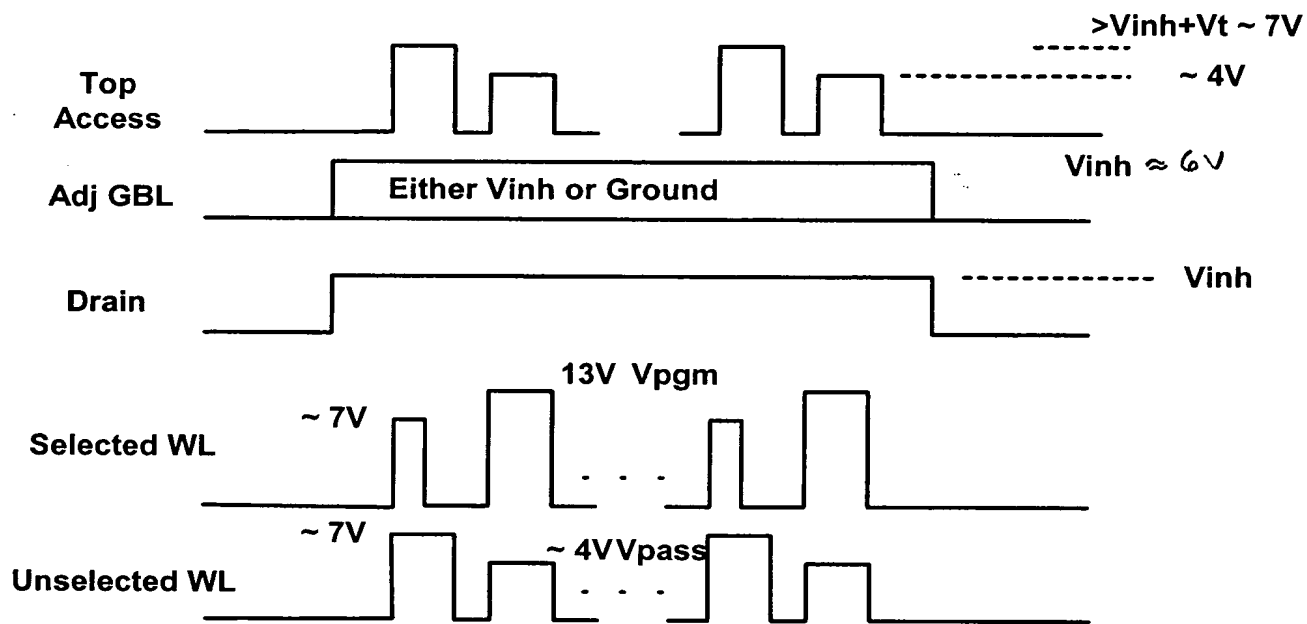


Fig. 7

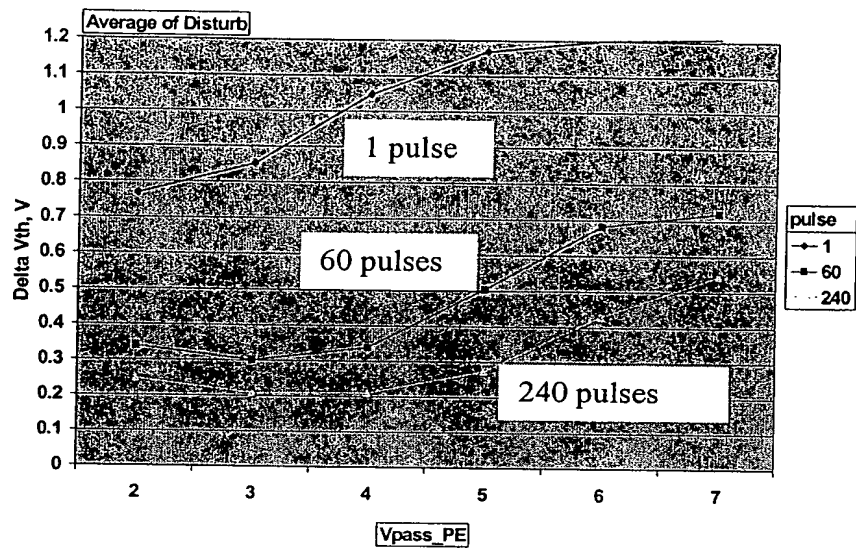
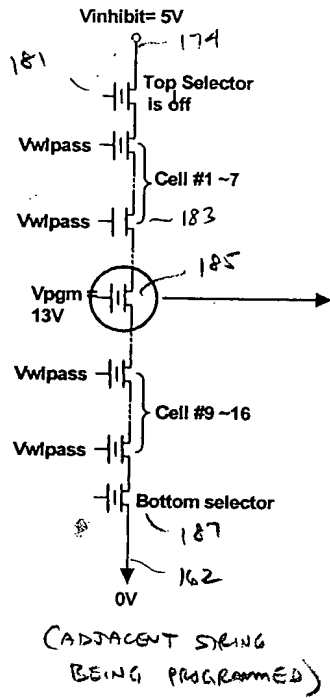


Fig. 8



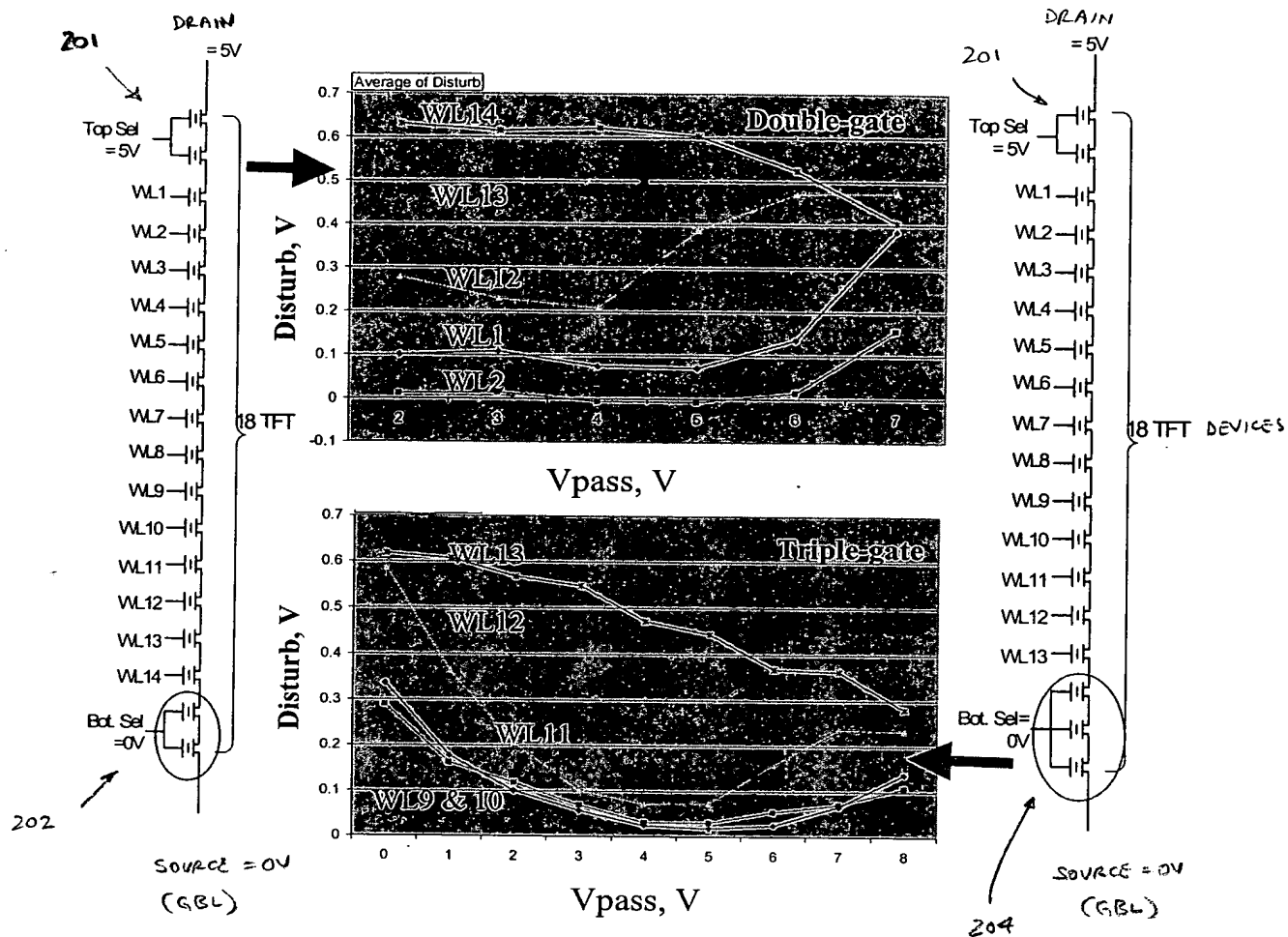


Fig. 9

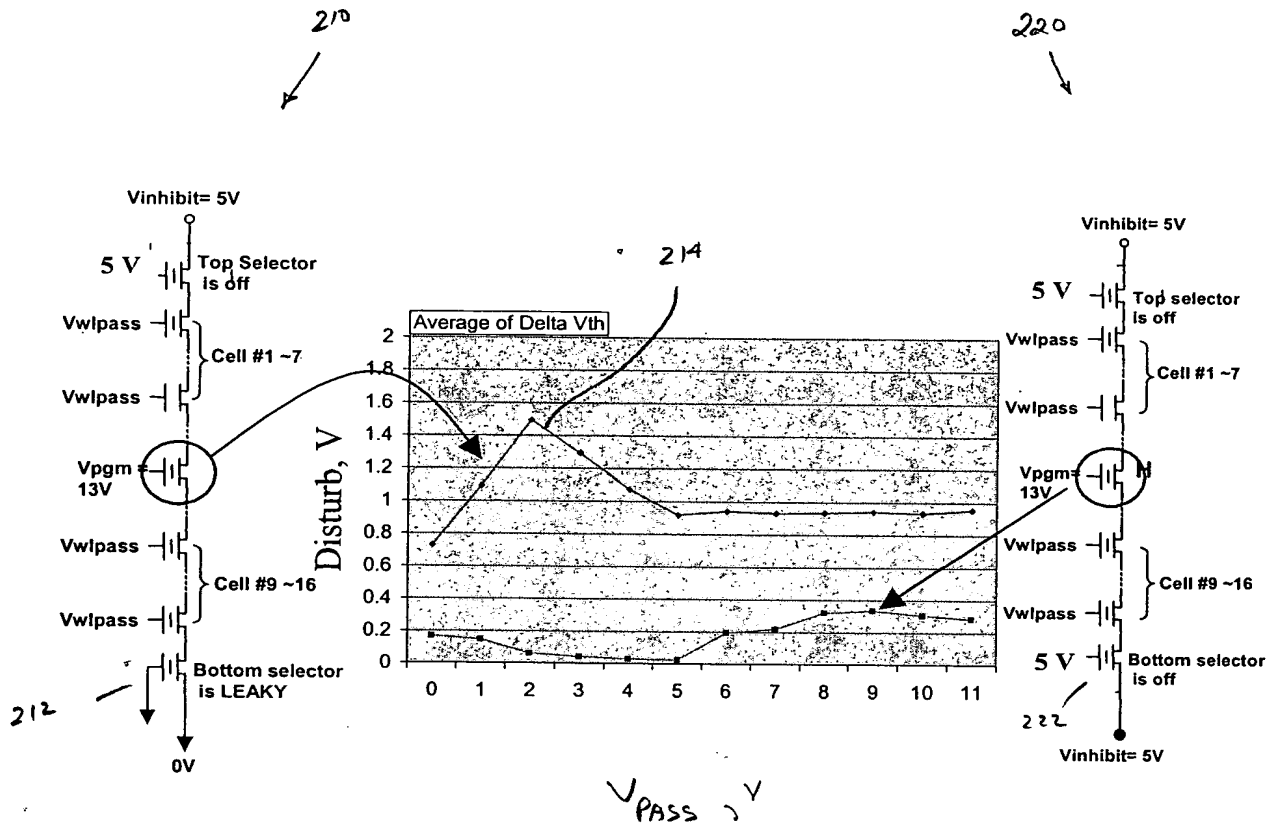


Fig. 10

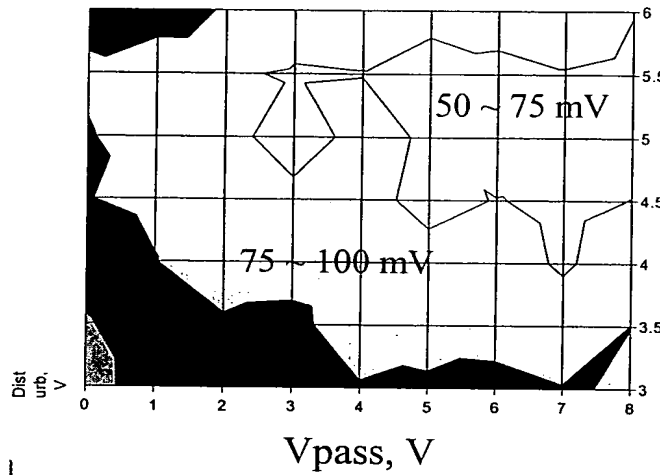


Fig. 11

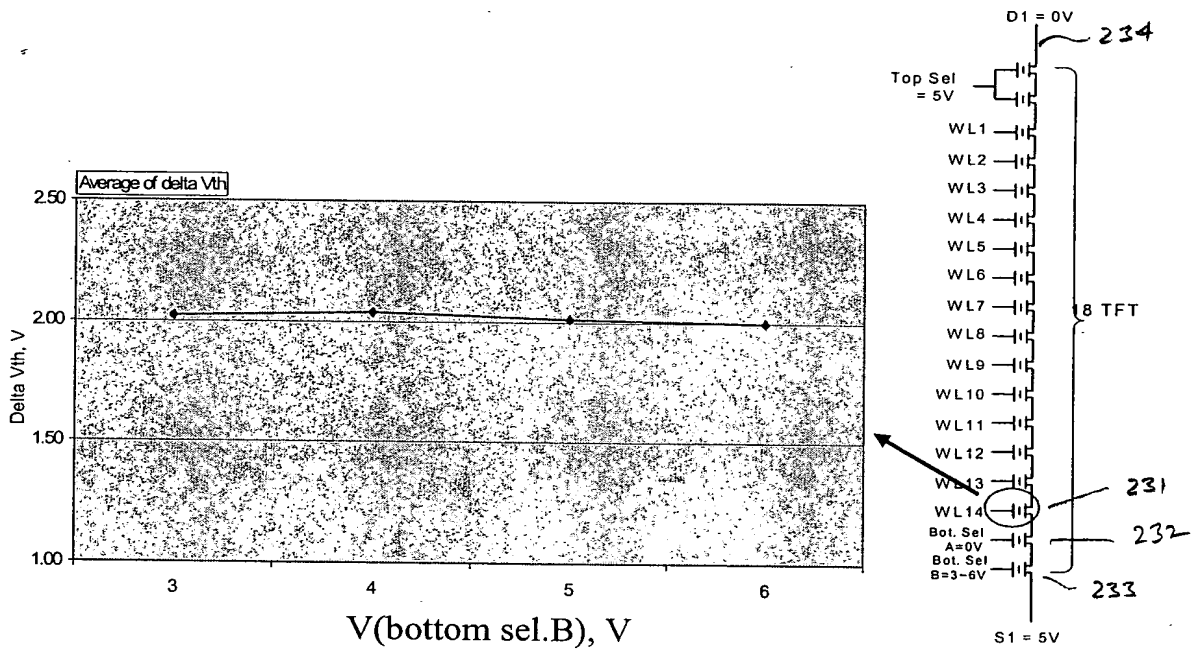
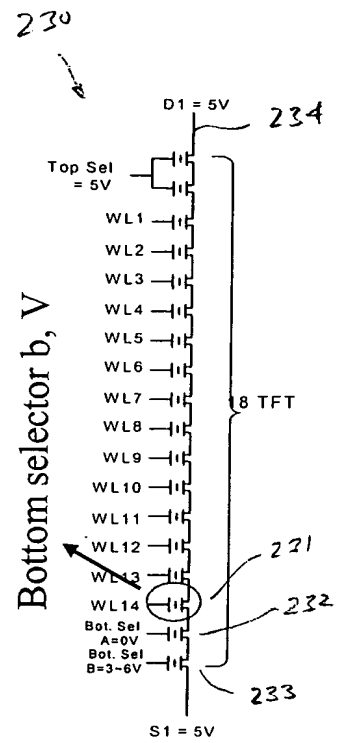


Fig. 12

250

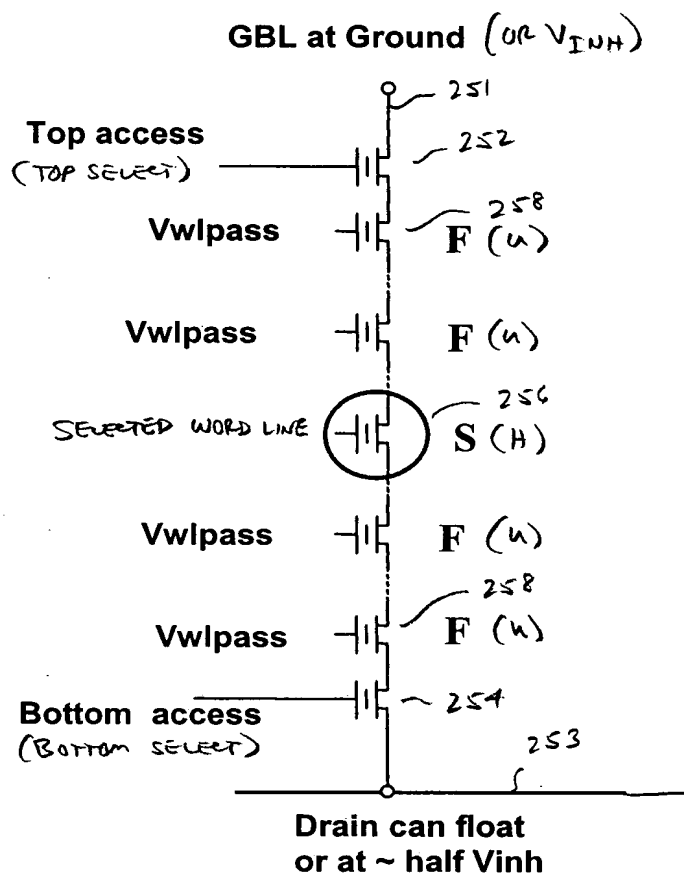


Fig. 13

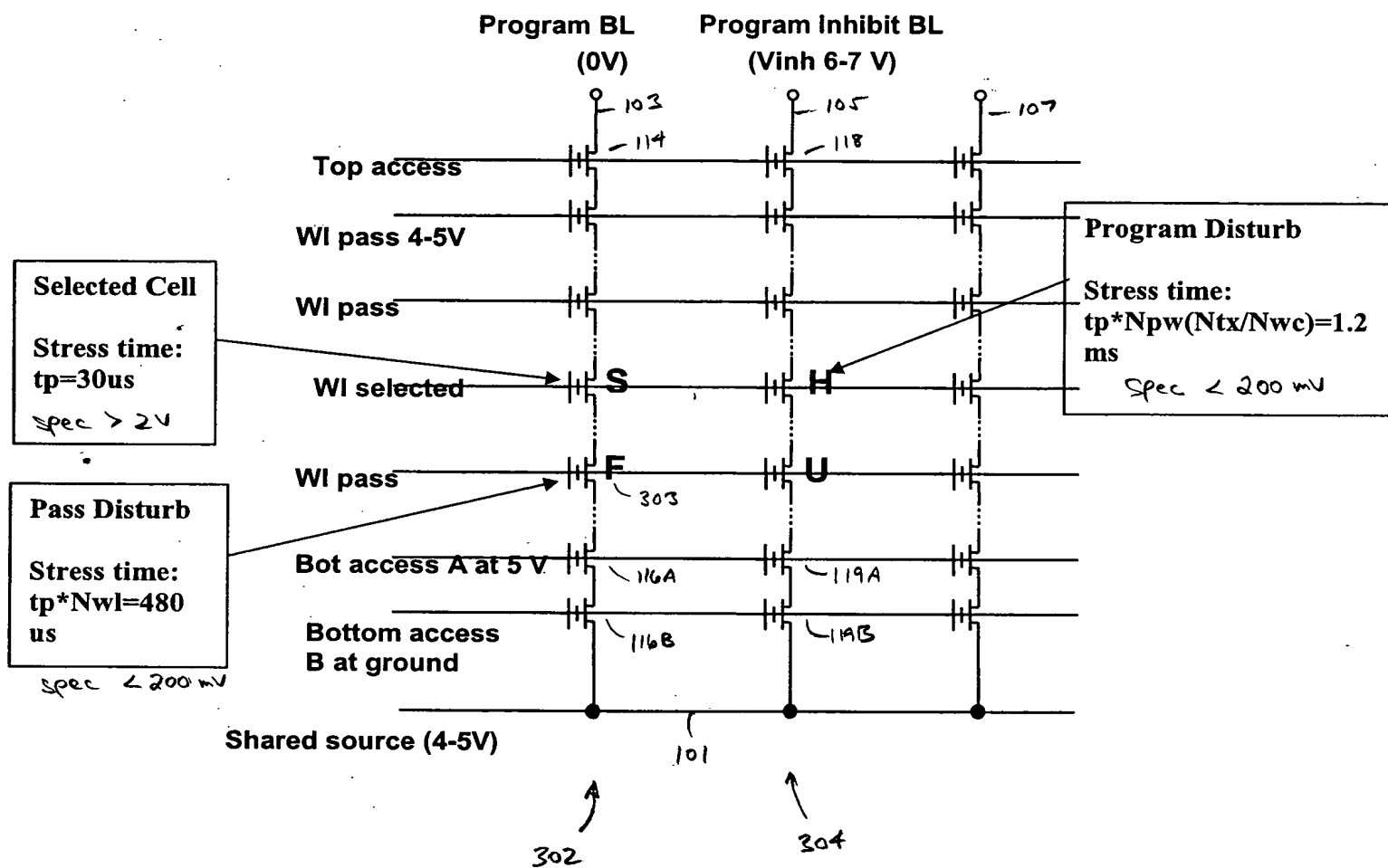
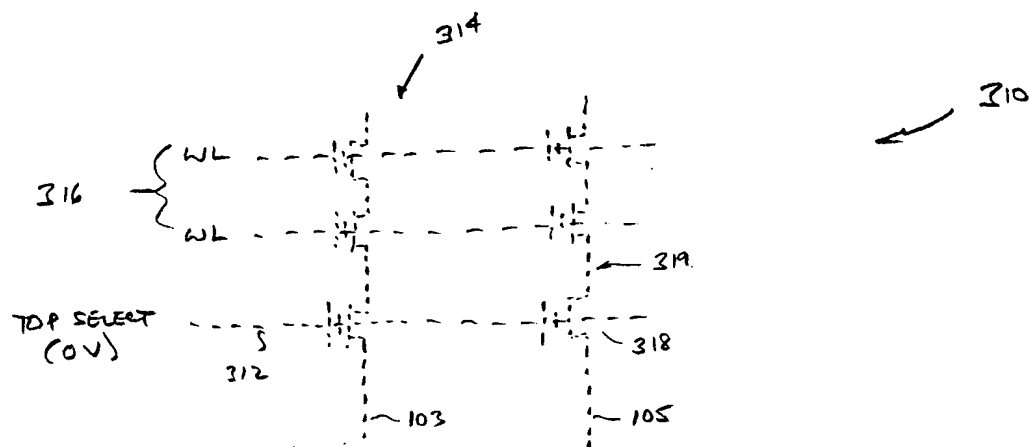


Fig. 14

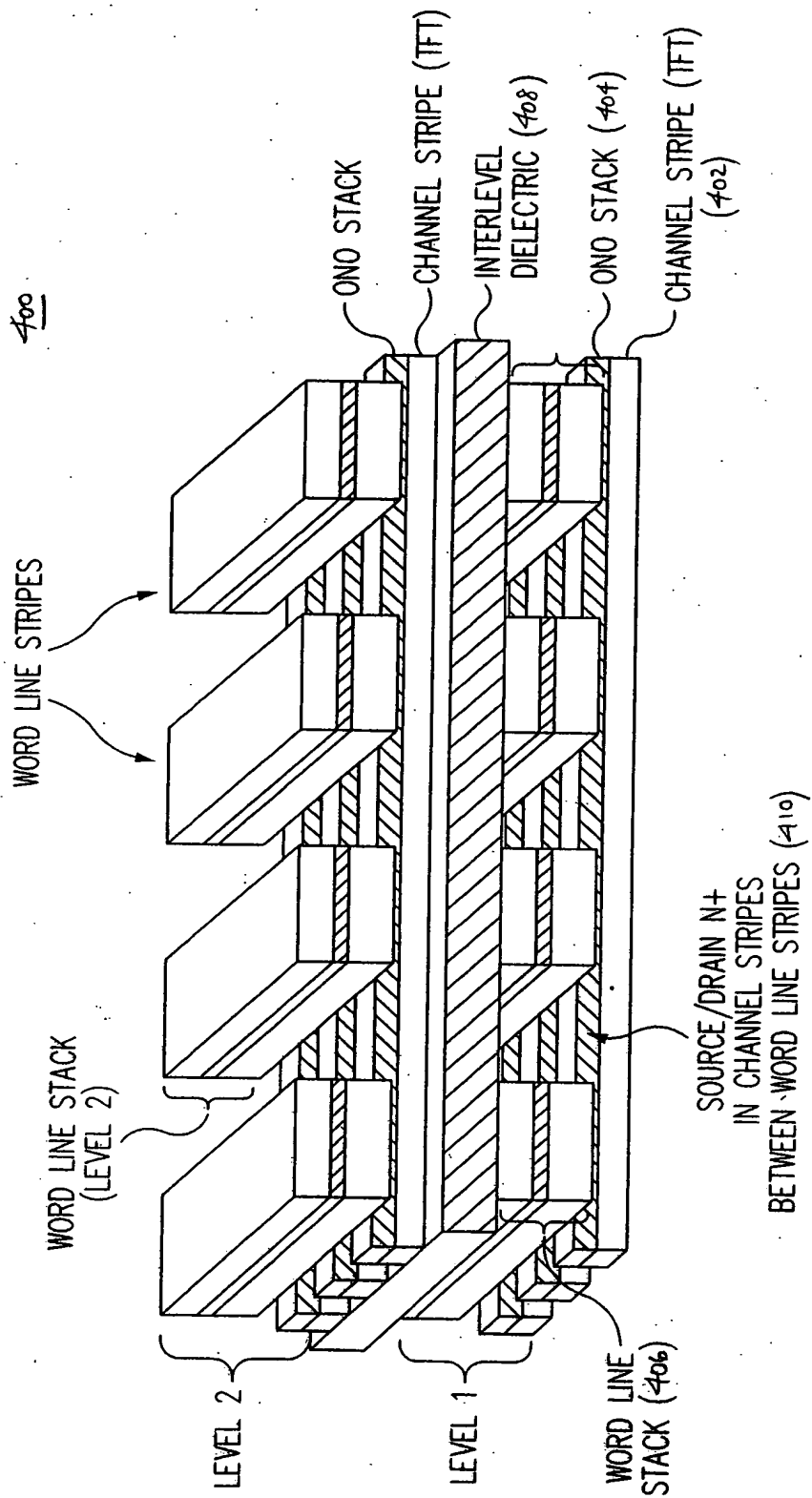


Fig. 15

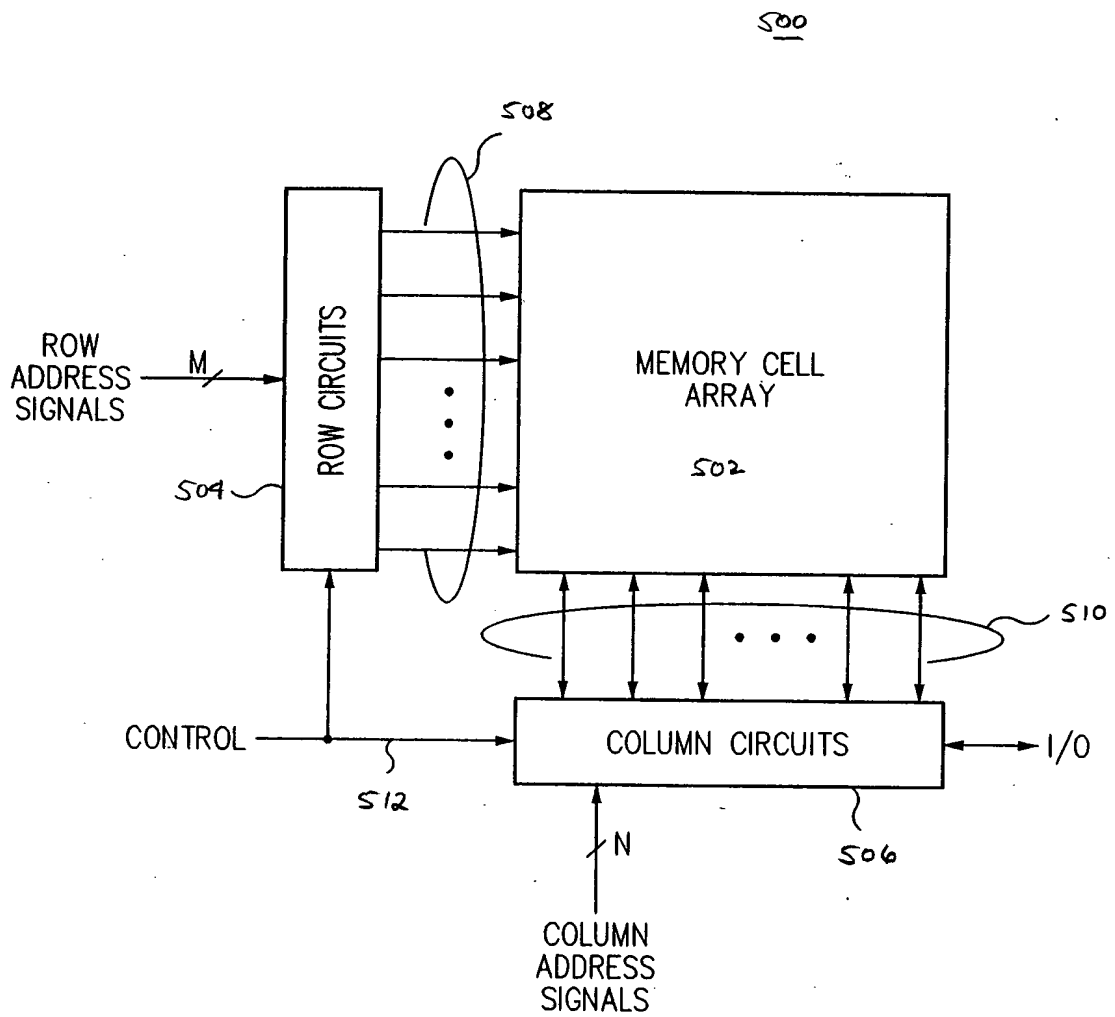
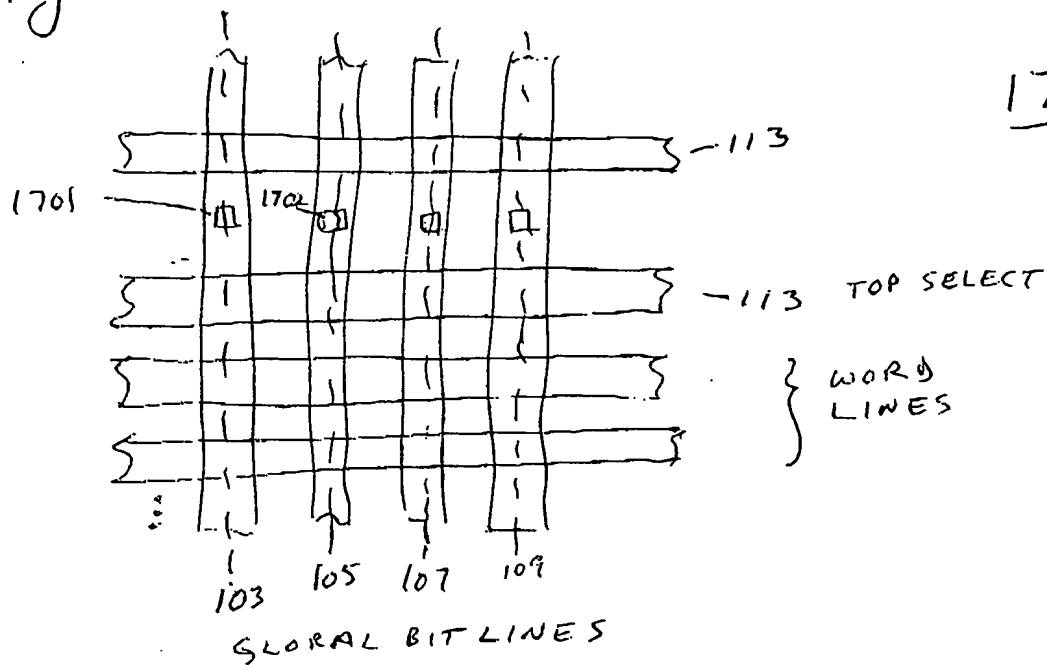


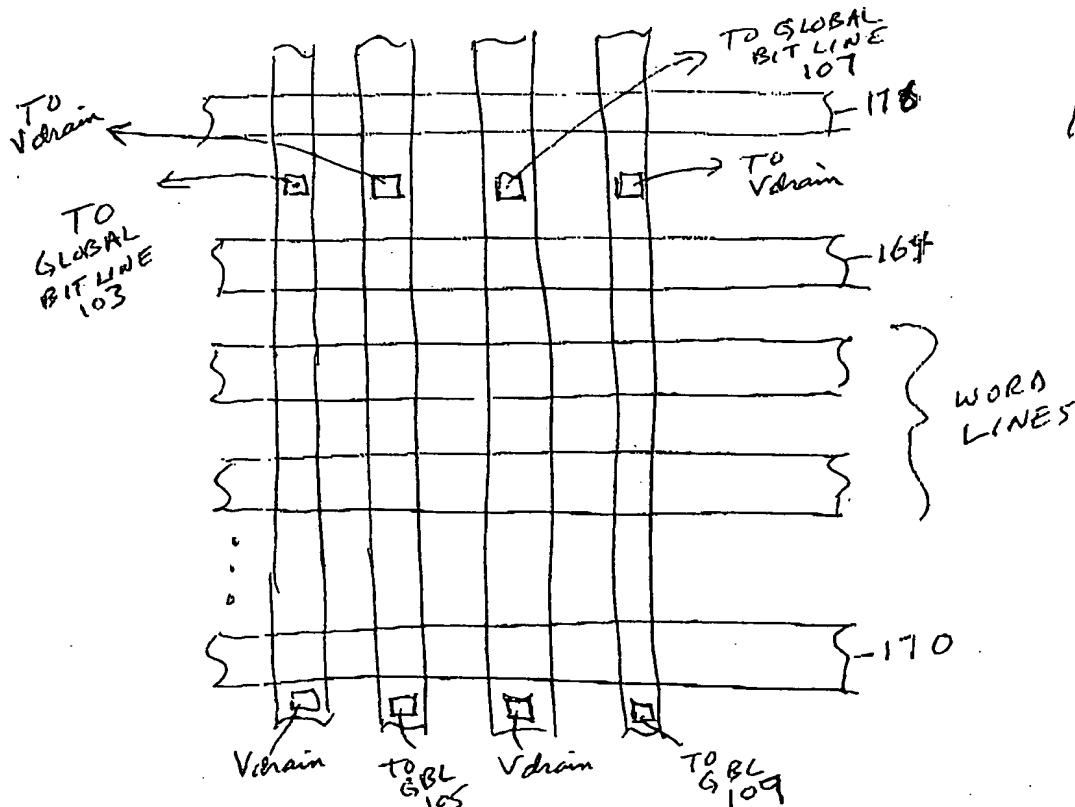
FIG. 16

Fig 17



17A

GLOBAL BIT LINES 103, 107 ON ONE LAYER }  
GLOBAL BIT LINES 105, 109 ON 2ND LAYER } 17 B



17 E



Fig 17

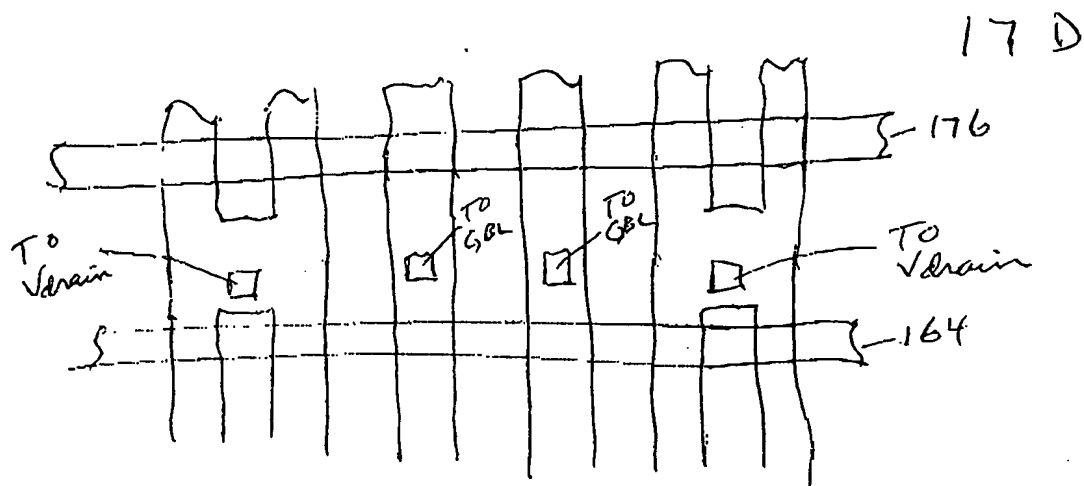
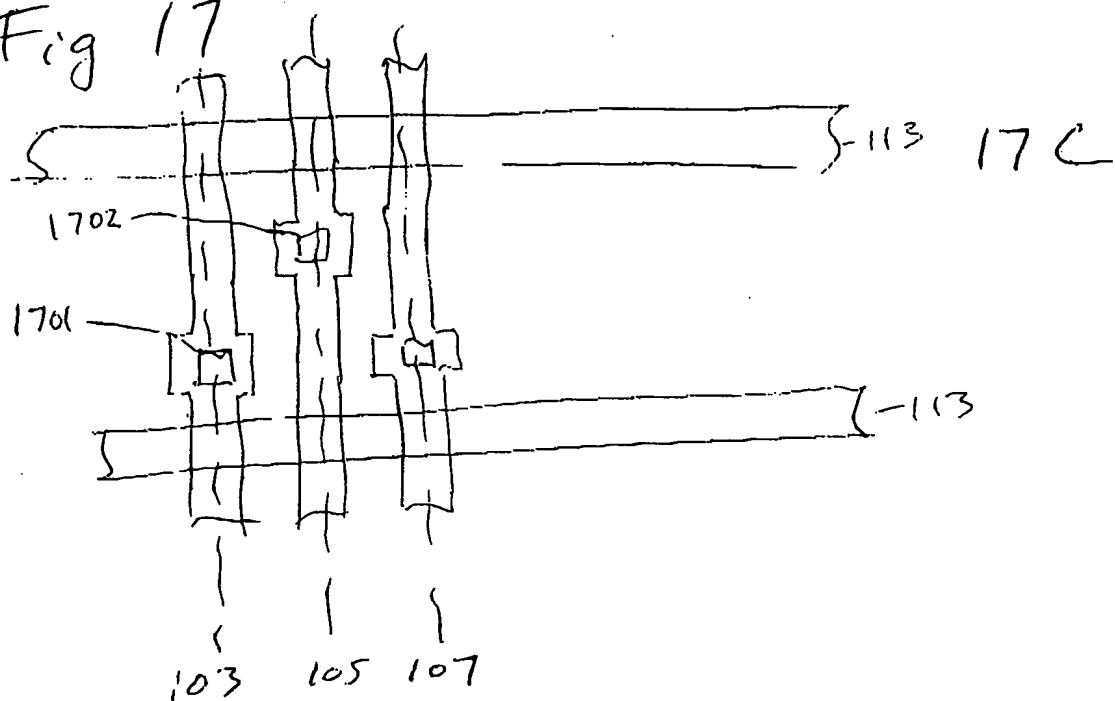


Fig. 18

